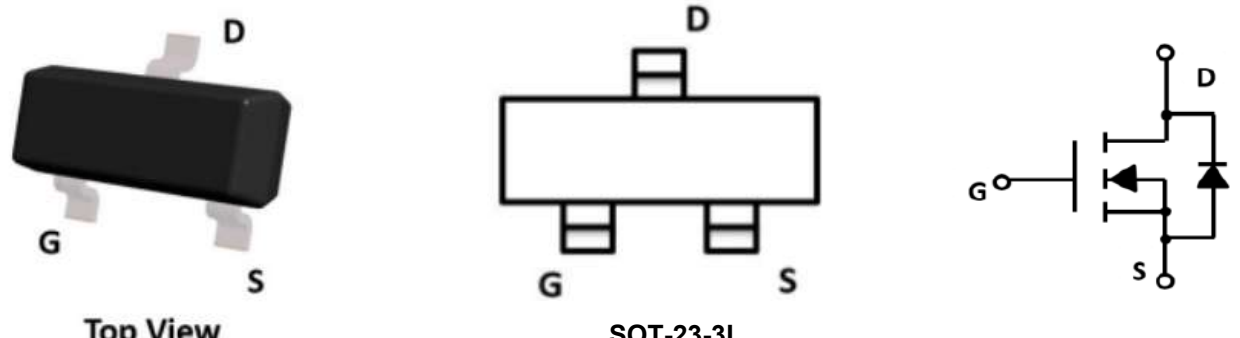


N-Channel Enhancement Mode Field Effect Transistor

Product Summary <ul style="list-style-type: none"> 20V, 8A $R_{DS(ON)Typ.} = 11m\Omega @ V_{GS} = 4.5V$ $R_{DS(ON)Typ.} = 16m\Omega @ V_{GS} = 2.5V$ Advanced Trench Technology Excellent $R_{DS(ON)}$ and Low Gate Charge Lead free product is acquired 	Applications <ul style="list-style-type: none"> Load Switch PWM Application Power management
 <p style="display: flex; justify-content: space-around; margin-top: 10px;"> Top View SOT-23-3L </p>	

Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Max.	Units
V_{DSS}	Drain-Source Voltage	20	V
V_{GSS}	Gate-Source Voltage	± 12	V
I_D	Continuous Drain Current	$T_A = 25^\circ C$	8
		$T_A = 100^\circ C$	5
I_{DM}	Pulsed Drain Current <small>note1</small>	32	A
P_D	Power Dissipation	$T_A = 25^\circ C$	1.5
$R_{\theta JA}$	Thermal Resistance, Junction to Case	83.3	$^\circ C/W$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ C$

Ordering Information

Part NO.	SI014N02T
Marking	14N02T
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=20V, V_{GS}=0V,$	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 12V$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	0.75	1.2	V
$R_{DS(on)}$	Static Drain-Source on-Resistance <small>note2</small>	$V_{GS}=4.5V, I_D=8A$	-	11	14	m Ω
		$V_{GS}=2.5V, I_D=5A$	-	16	22.5	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=10V, V_{GS}=0V,$ $f=1.0MHz$	-	1000	-	pF
C_{oss}	Output Capacitance		-	182	-	pF
C_{rss}	Reverse Transfer Capacitance		-	164	-	pF
Q_g	Total Gate Charge	$V_{DS}=10V, I_D=4A,$ $V_{GS}=4.5V$	-	15	-	nC
Q_{gs}	Gate-Source Charge		-	2	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	5.2	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=10V,$ $I_D=4A, R_{GEN}=3\Omega,$ $V_{GS}=4.5V$	-	9	-	ns
t_r	Turn-on Rise Time		-	25	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	37	-	ns
t_f	Turn-off Fall Time		-	14	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	8	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	32	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=8A$	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

 2. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

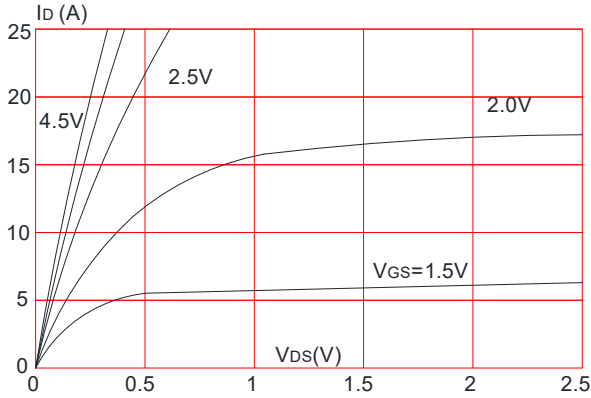


Figure 2: Typical Transfer Characteristics

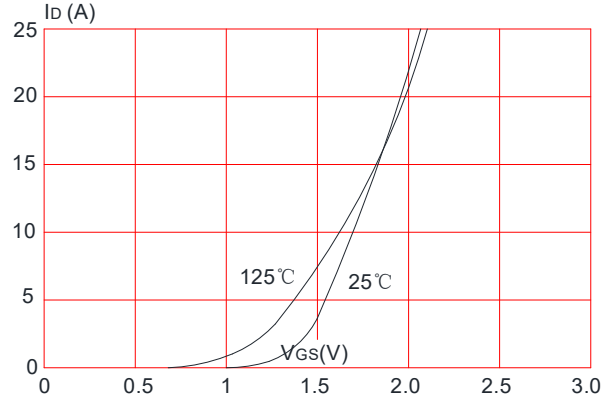


Figure 3: On-resistance vs. Drain Current

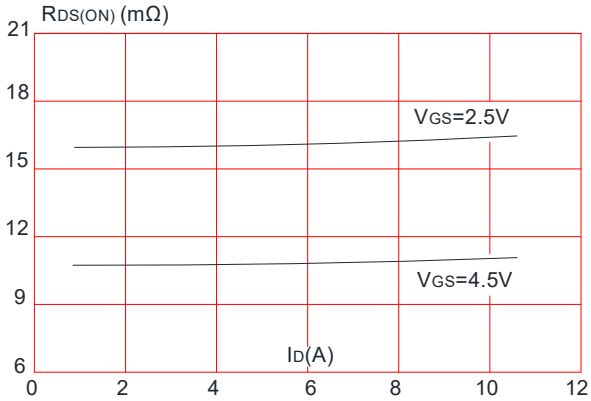


Figure 4: Body Diode Characteristics

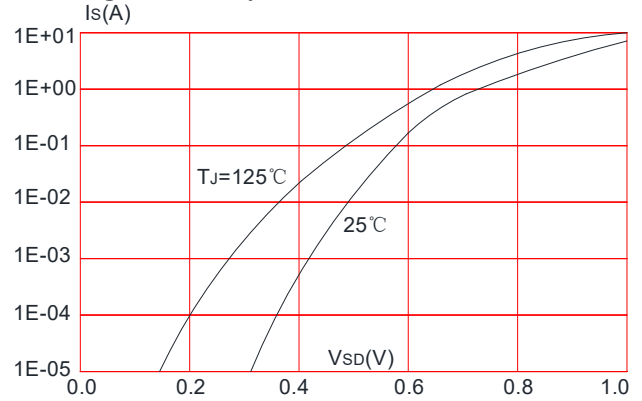


Figure 5: Gate Charge Characteristics

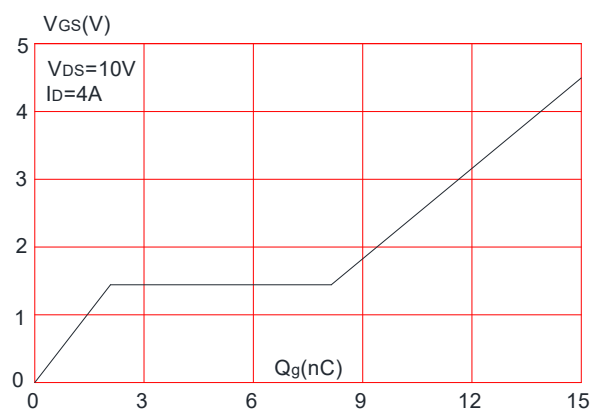


Figure 6: Capacitance Characteristics

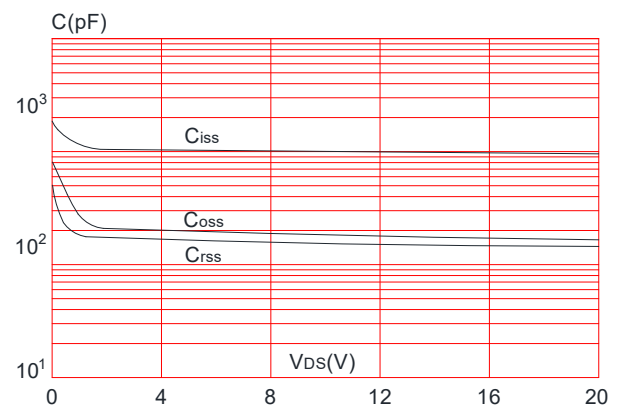


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

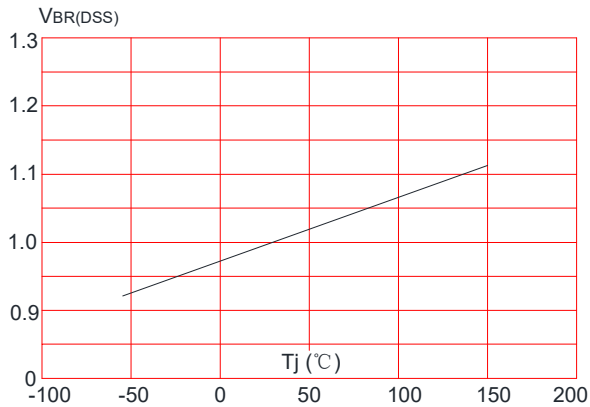


Figure 8: Normalized on Resistance vs. Junction Temperature

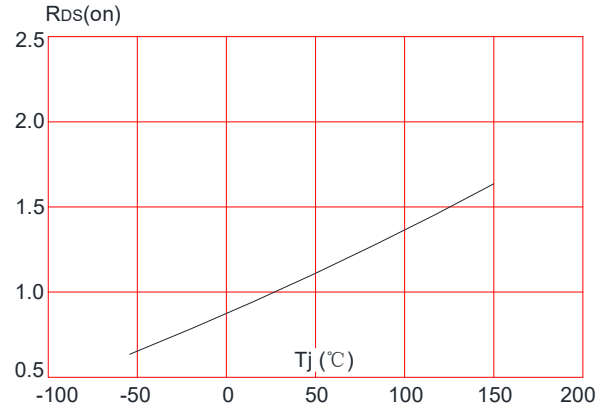


Figure 9: Maximum Safe Operating Area

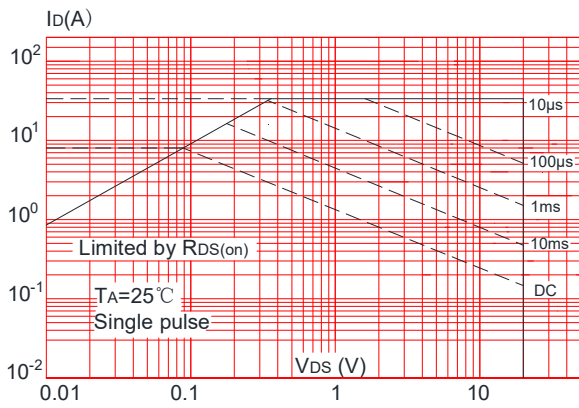


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

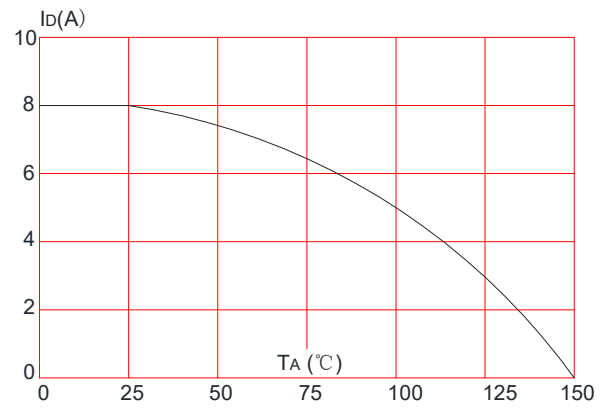
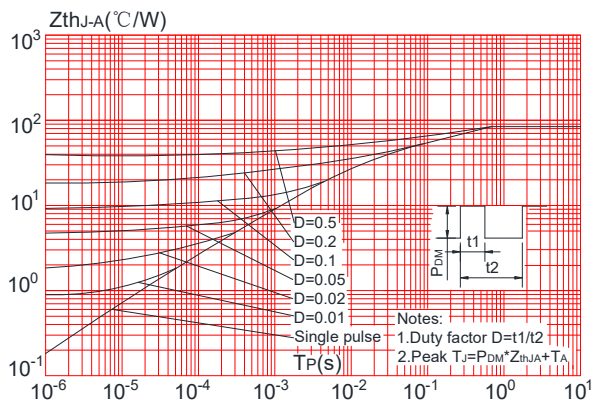
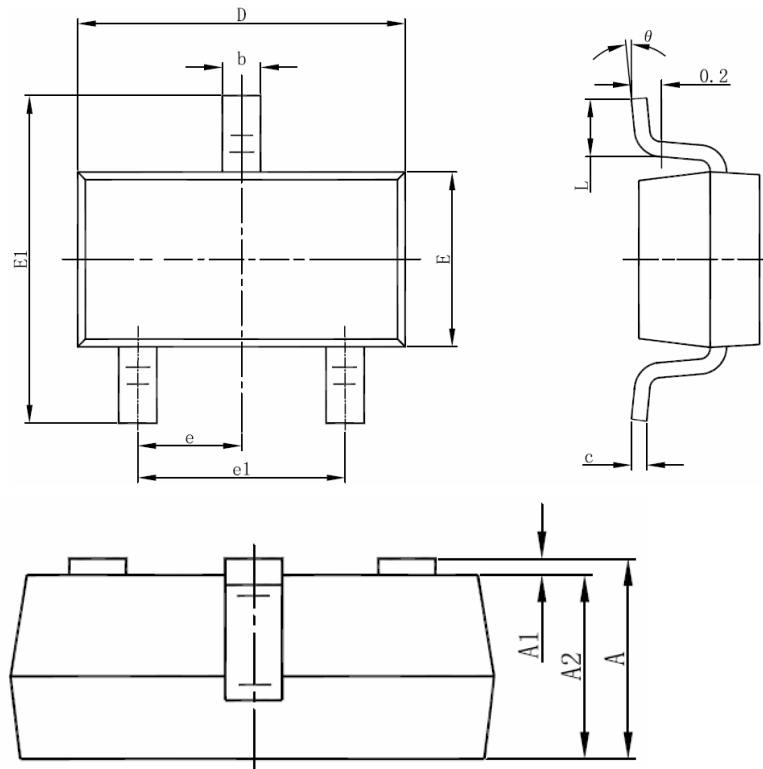


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



■ SOT-23-3L Package information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°